Department of Electrical and Computer Engineering
ECE 254B - Advanced Computer Architecture: Reconfigurable Computing
Spring 2007

Instructor: Prof. Ryan Kastner (kastner@ece)
Time: Monday, Wednesday, 12-2pm
Location: Phelps 1437
Office Hours: After class or by appointment

Grading:
  · Project - 70%
    · 15% - Topic research - Literature survey
    · 25% - Project Presentations - 10 minute progress report & 15 minute presentation during dead week
    · 30% - Project Reports (progress and final reports)
  · Homework - 20%
  · Class Participation - 10%
    · Attendance
    · Questions in class, especially during student presentations

Class Materials:
  · Lectures
  · Reader
  · Student Projects

Prerequisites:
This class requires a background knowledge found in a typical undergraduate computer engineering curriculum. Specifically, the class assumes knowledge of compilers, algorithms, computer architecture and low level hardware synthesis (logic and physical synthesis).